

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**IN RE APPLICATION OF:
YIN TAT MA**

SERIAL NO. : 10/501,651

FILED: JAN. 10, 2005

**FOR: ON-CHIP ESD PROTECTION
CIRCUIT FOR COMPOUND
SEMICONDUCTOR
HETEROJUNCTION BIPOLAR
TRANSISTOR RF CIRCUITS**

Examiner: D.H. Patel

Group Art Unit: 2836

REQUEST FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In further reply to the final Office Action mailed Feb. 2, 2007, please grant an extension of time of one month, namely until June 2, 2007, to allow filing of a copending continuation/divisional application and to allow the Examiner additional time to enter the Amendment After Final and issue a Notice of Allowance. A check for the appropriate extension fee is attached. The Office is also authorized to charge all fees under 37 CFR § 1.17, or all required extension of time fees or any deficit and credit any surplus in such fees to our deposit account #01-1960.

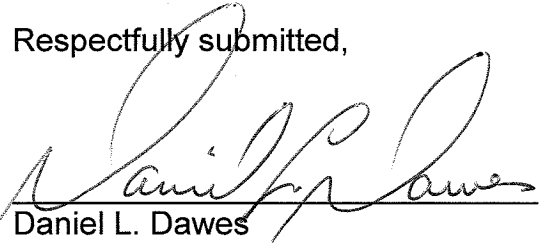
Applicant respectfully requests advancement of the claims to allowance.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 8, 2007 by

Signature

May 8, 2007

Respectfully submitted,



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